WEST Search History

| Hide Items | Restore | Clear | Cancel |

DATE: Tuesday, December 05, 2006

Hide?	Set Name	Query	Hit Count
		DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ	
☐	L12	L11 and l2	4
☐	L11	sony.as. and ((parallel or simultaneous$) near5 process$) and (stream near5 video)	193
☐	L10	L9 and ((parallel or simultaneous$) near5 process$)	3
☐	L9	(Dorojevets or Ogura).in. and (vector near5 register)	7
☐	L8	((process$ near5 slice) same (alu or arithmetic logic unit) and ((read$ or register$ or stor$) near5 vector) and (stream near5 (video or picture)) same $4block)	2
☐	L7	vector register and block register and scalar register and arithmetic unit and ((simultaneous$ or parallel or pipeline) near7 process$)	1
☐	L6	L3 and l4	1
☐	L5	L4 and l2	4
☐	L4	((vector register same (block or macroblock)) and ((alu or arithmetic logic unit) same (process$ or co$process$ or pipeline or (identical or parallel or simultaneous$)) adj4 process$))	13
☐	L3	L2 and (((stream or packet) near9 video) same slice)	31
☐	L2	(((parallel or simultaneous$ or identical$) near7 process$) same vector) and (video near5 process$)	640
☐	L1	(345/$.ccls. or 348/$.ccls.) and ((processing element or co$processor or pipeline or (identical or parallel) processor) same vector register same scalar register)	1

END OF SEARCH HISTORY

Refine Search

Search Results -

Terms	Documents
(two-dimension$ array same processing element same arithmetic logic unit).clm.	1

Database:

US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

Search: L3

Refine Search

Recall Text   Clear   Interrupt

Search History

DATE: Tuesday, December 05, 2006   **Purge Queries**   Printable Copy   Create Case

Set Name side by side	Query	Hit Count	Set Name result set
DB=PGPB; PLUR=YES; OP=ADJ			
L3	(two-dimension$ array same processing element same arithmetic logic unit).clm.	1	L3
L2	(two-dimension$ array same processing elelment same arithmetic logic unit).clm.	0	L2
L1	(processing element and vector register and block register and scalar register).clm.	2	L1

END OF SEARCH HISTORY